

;rozsireni vystupu 8051 obvodem 74164 na 16bitu

```
    org    0

Count  equ    16           ;hodnota pocitadla

Init:   mov    r4, #Count
        setb  P0.0        ;nastaveni log.1 na P0.0 => LED nesviti

Clear:  setb  P0.1
        clr   P0.1
        djnz r4, Clear    ;16x opakuje => log.1 na P0.0

        mov   r4, #Count
        mov   a, #11100111b ;hodnota, která bude na rozsirenem vystupu,
        clr   c           ;bude 2x za sebou => 2x8bitu

Main:   rlc    a
        jnc   Zero

One:    setb  P0.0
        jmp   Timer

Zero:   clr   P0.0 ;

Timer:  call   Delay2      ;zacatek clocku
        setb  P0.1
        call  Delay2
        clr   P0.1      ;konec clocku

        djnz  r4, Main

        jmp   $

Delay2: mov    r7, #07h    ;nastaveni zpozdeni 0,5s
        mov    r6, #00h
        mov    r5, #00h

Delay:  djnz  r5, Delay    ;zpozdeni 0,5s
        djnz  r6, Delay
        djnz  r7, Delay
        ret

        nop
        end
```

